

CLAIM AMENDMENTS

1. (Currently Amended) A computer storage device ~~medium~~ having processor-executable instructions that, when executed by a processor, perform acts ~~a method~~ comprising:

~~observing and determining a location in a processor-readable memory of a computer, where a watermark detector that is a dynamic embedded-watermark signal detection program module ("watermark detector") receives an subject input stream for the watermark detector to perform detection thereon to determine if the input stream has an embedded-watermark signal therein; and~~

~~intervening with embedded-watermark signal detection by the watermark detector by varying an audio sample rate or a video frame rate clear reception of the subject input stream so that the watermark detector receives the input stream at a variable rate, thereby hindering watermark detection by the watermark detector.~~

2. (Canceled).

3. (Canceled).

4. (Currently Amended) A computer storage device ~~medium~~ as recited in claim 1, wherein the intervening further comprises introducing a countersignal into the input ~~incoming~~ stream.

5. (Currently Amended) A computer storage device-medium as recited in claim 1, wherein the intervening further comprises introducing noise into the input incoming stream.

6. (Currently Amended) A computer storage device-medium as recited in claim 1, further comprising maintaining the intervening of the watermark detection by the watermark detector while the input stream is being consumed.

7. (Currently Amended) A computer storage device-medium as recited in claim 1, wherein the ~~type of the subject~~ input stream includes at least one-is selected from a group consisting of image, audio, video, multimedia, software, metadata, and data.

8. (Currently Amended) A computing device comprising:
an input device for receiving one or more input streams; and
~~a medium as recited in claim 1~~
a computer storage device having processor-executable instructions that, when executed by a processor, perform acts comprising:
determining a location in a processor-readable memory where a watermark detector that is a dynamic embedded-watermark signal detection program module receives an input stream to determine if the input stream has an embedded-watermark signal; and

intervening with watermark detection by the watermark detector by continuously varying an audio sample rate or a video frame rate of the input stream.

9. (Currently Amended) A method facilitating circumvention of ~~dynamic, robust,~~ embedded-signal detection, the method comprising:

~~observing and determining a location in a processor-readable memory of a computer, where a watermark detector that is a dynamic embedded-watermark signal detection program module ("watermark detector") receives an subject input stream for the watermark detector to perform detection thereon to determine if the input stream has an embedded-watermark signal therein; and~~

intervening with embedded-watermark signal detection by the watermark detector by continuously varying an audio sample rate or a video frame rate ~~clear~~ reception of the subject input stream, ~~thereby hindering watermark detection by the watermark detector.~~

10. (Canceled).

11. (Canceled).

12. (Currently Amended) A method as recited in claim 9, wherein the intervening further comprises introducing a countersignal into the input ~~incoming~~ stream.

13. (Currently Amended) A method as recited in claim 9, wherein the intervening further comprises introducing noise into the input-incoming stream.

14. (Currently Amended) A method as recited in claim 9, further comprising maintaining the intervening of the watermark detection by the watermark detector while the input stream is being consumed.

15. (Currently Amended) A method as recited in claim 9, wherein the ~~type of~~ the subject input stream includes at least one ~~is selected from a group consisting of~~ image, audio, video, multimedia, software, metadata, and data.

16. (Original) A computing device comprising one or more processor-readable media having processor-executable instructions that, when executed by the computer, perform the method as recited in claim 9.

17. (Currently Amended) A system facilitating circumvention of ~~dynamic, robust, embedded-signal~~ ("watermark") detection, the system comprising:

~~a watermark-detector detector that memory-location determiner ("watermark-detector detector") configured to determines~~ a memory location where a watermark detector that is a dynamic embedded-signal detection program module (~~"watermark detector")~~ receives an ~~subject input stream for the watermark detector to perform detection thereon to determine if the stream has an embedded-watermark signal therein;~~

an intervention component ~~that configured to~~ intervenes with ~~embedded-watermark signal detection-clear reception of the subject input stream~~ by the watermark detector ~~during a playback of the input stream by providing a countersignal stream that lacks the embedded-watermark signal to the memory location,~~ thereby hindering watermark detection by the watermark detector.

18. (Currently Amended) A system as recited in claim 17, wherein the watermark-detector detector is further configured to detect and observe the watermark detector in a processor-readable memory of a computer to determine the memory ~~its location in such memory~~.

19. (Currently Amended) A system as recited in claim 17, wherein the ~~intervention by the intervention component~~ further adjusts an audio sample rate or a video frame rate ~~includes adjusting "play-rate" of the input-incoming stream~~.

20. (Canceled).

21. (Currently Amended) A system as recited in claim 17, wherein the intervention component ~~is further configured to~~ introduces noise into the input-incoming stream.

22. (Currently Amended) A system as recited in claim 17, wherein the type of the subject input stream includes at least one ~~is selected from a group consisting of~~ image, audio, video, multimedia, software, metadata, and data.

23-45. (Canceled).

46. (Currently Amended) A computer storage device ~~medium~~ having computer-executable instructions that, when executed by a computer, perform a method for facilitating circumvention of watermark detection, the method comprising:

determining a memory location ~~where~~, in a processor-readable memory, where a watermark detector that is a dynamic watermark detection program module ("watermark detector") receives an subject input stream ~~for the watermark detector to perform watermark detection thereon~~ to determine if the subject input stream has a watermark therein;

observing the watermark detector in the processor-readable memory of a computer to determine the its location in such memory location;

varying an audio sample rate ~~intervening with clear reception~~ of the subject input stream continuously to hinder, ~~thereby hindering~~ watermark detection by the watermark detector, ~~wherein the intervening comprises adjusting "play rate" of the input stream.~~

47. (Currently Amended) A method for facilitating circumvention of ~~dynamic~~, robust, embedded-signal detection, the method comprising:

~~observing_dynamic_detector_that_is~~ a dynamic embedded-watermark signal detection program module (~~"dynamic_detector"~~) in a processor-readable memory of a computer ~~that configured to dynamically detects~~ watermarks in an input stream;

based upon the observing, determining a location in the processor-readable memory, ~~the location being where the dynamic detector receives a subject incoming the~~ input stream for the dynamic detector to perform embedded-watermark signal detection thereon to determine if the ~~subject incoming input~~ stream has an embedded-watermark signal therein; and

~~varying a video frame rate intervening with clear reception of the input-subject incoming stream continuously to hinder, thereby hindering~~ embedded-watermark signal detection by the dynamic detector, ~~wherein the intervening comprises adjusting "consumption rate" of the incoming stream.~~

48. (Currently Amended) A system for facilitating circumvention of ~~dynamic, robust,~~ embedded-signal detection, the system comprising:

an input device configured to receive one or more input streams;

a ~~watermark-detector detector that~~ memory-location-determiner (~~"watermark-detector-detector"~~) configured to determines a memory location ~~where, in a processor-readable memory, where a watermark detector that is an~~ embedded-signal detection program module (~~"detector"~~) receives an input stream ~~subject input stream for the detector to perform detection thereon~~ to determine if the ~~subject~~ input stream has an embedded-watermark signal therein ~~and further configured to detect and observe the~~

~~detector in a processor-readable memory of a computer to determine its location in such memory; and~~

~~an intervention component that configured to intervenes with embedded-watermark detection clear reception of the subject input stream, thereby hindering watermark detection by the watermark detector during a playback of the input stream by providing a countersignal stream that lacks the embedded-watermark signal to the memory location, wherein the intervening comprises adjusting an incoming rate for the input stream.~~

49. – 54. (Canceled).